

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Toshiki Makimoto, et al.	)
		)
Serial No.:	10/516,380	) Art Unit
		) 2818
Filed:	November 30, 2004	)
		)
Confirmation No.:	2860	)
		)
For:	P-TYPE NITRIDE SEMICONDUCTOR STRUCTURE AND BIPOLAR TRANSISTOR	)
		)
Examiner:	Tram Nguyen	)

TRANSMITTAL

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

Applicant submits herewith the issue fee for the above identified patent application based on the replacement Notice of Allowability received with an Office communication dated October 14, 2009. In the Office communication, the Examiner acknowledged the error in the original Notice of Allowability regarding the list of allowed claims. The Office communication acknowledged that the allowed claims should be claims 50, 51, 53, 56, 77, 78, 80-82, 85-89, and 96.

Applicant notes that the replacement Notice of Allowability has a new error. The replacement Notice of Allowability includes the claims listed above as being allowed, except for the following error: the replacement Notice of Allowability erroneously lists claim 79 as being allowed instead of claim 78. In a subsequent telephone conversation, the Examiner admitted that this was a mistake and that the claims listed in the Office communication (i.e., claims 50, 51, 53, 56, 77, 78, 80-82, 85-89, and 96) constitute the allowed claims. Applicant respectfully requests that the Examiner correct this mistake before issuance of the patent.

Dated this 27th day of October 2009.

Respectfully submitted,

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